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(54) **Liquid crystal display unit with a plurality of subpixels.**

(57) A liquid crystal display unit including a plurality of subpixels (21a, 21b) in each pixel (21) provides superior gradation over a wide viewing angle, and is produced with no increase in manufacturing cost and no deterioration of desired properties (such as reduction in brightness, contrast and response). No additional manufacturing step is required since a control capacitor electrode (8c) is formed at the same time as source and drain electrodes (8a, 8b) constituting a thin-film transistor (24). By controlling the ratio of display area and the difference in applied voltage of the subpixels (21a, 21b), preferable viewing angle properties are obtained. The display properties at the boundary of subpixels are improved by controlling the gap width and the direction of boundary of the subpixels (25), thus providing a bright display.

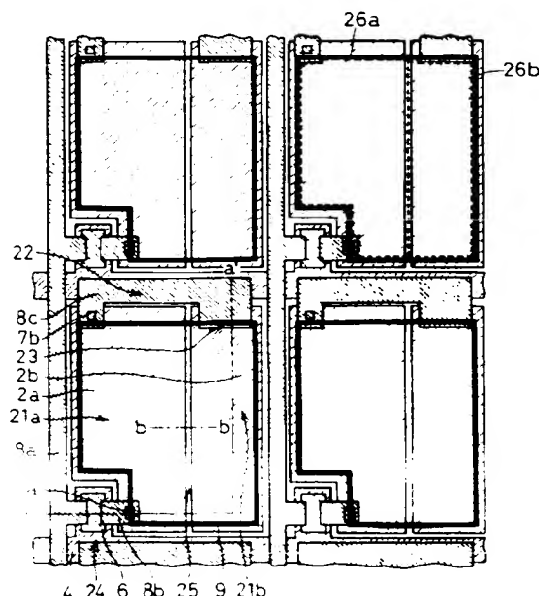


FIG 1 (a)

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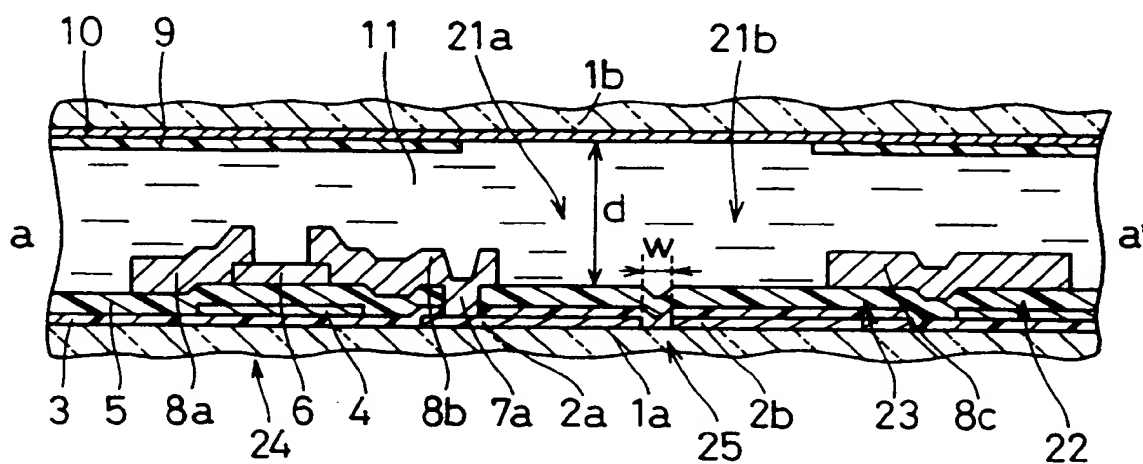


FIG. 1 (b)

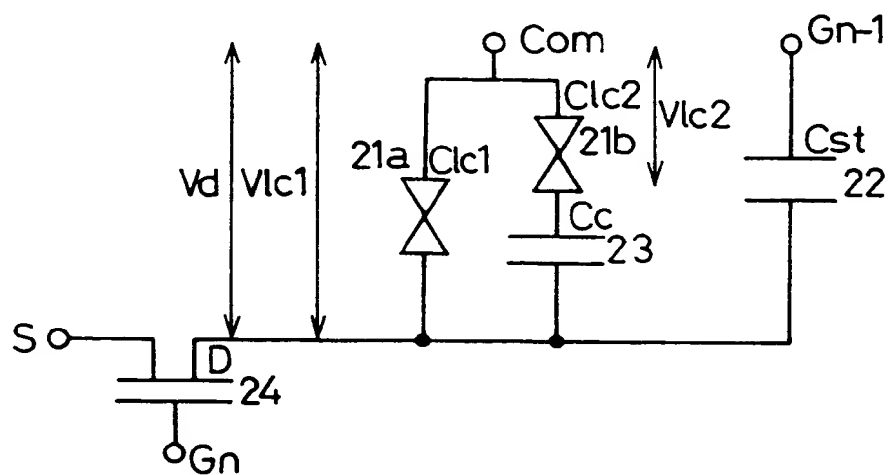


FIG.1 (c)

This invention relates to a liquid crystal display unit in which each pixel is subdivided into a plurality of subpixels, and voltage applied to a liquid crystal layer of each subpixel is different from that of other subpixels, thus achieving a preferable gradation display over a wide viewing angle.

A conventional example of a liquid crystal display unit in which each pixel includes subpixels is disclosed in U.S. Pat. No. 4,840,460 (first conventional example). Fig. 11 (a) shows an equivalent circuit of the example. In the figure, 101 and 102 are gate wirings; 103 and 104 are source wirings; 105 is a thin-film transistor (TFT); and 106 is a common electrode. A pixel at the intersection of gate wiring 101 and source wiring 103 is indicated in the equivalent circuit. One pixel is divided into three subpixels. Control capacitors C_{c1} , C_{c2} and C_{c3} are coupled in series with effective capacitors C_{LC1} , C_{LC2} and C_{LC3} respectively. Voltage applied to a liquid crystal layer of each subpixel element is determined by the ratio between the capacitance of effective capacitor C_{LCi} and that of control capacitor C_i (where $i=1, 2$ and 3), when selection signals are provided to gate wiring 101 to turn on TFT 105 and voltage V is provided from source terminal 103. In other words, since this liquid crystal display unit includes subpixels with different operating voltage levels and displays by controlling the capacitance of the control capacitors of the subpixels, viewing angle properties are improved.

In the above-mentioned U.S. patent document, a specific structure for a subpixel using a divided subpixel electrode and control capacitor electrode is disclosed. Fig. 11 (b) is a cross-sectional view of the subpixel element in which a subpixel electrode 113 is divided into three sections and a control capacitor is formed by sandwiching an insulating film 114 between the subpixel electrode and a control capacitor electrode 115. The capacitance of the control capacitor is controlled at a preferable level by varying the area of control capacitor electrode 115 at each subpixel. The control capacitor electrode is coupled to the drain electrode of TFT by a terminal 116. Referring to the figure, 112 indicates a liquid crystal layer, forming an effective capacitor between each section of subpixel electrode 113 and a common electrode 111. The U.S. patent document also discloses a subpixel element in which a pixel electrode is not divided and an insulating film, having a thickness and a dielectric constant associated with one subpixel different from those associated with other subpixels, is formed between the pixel electrode and a liquid crystal layer so as to act as a control capacitor. However, in this structure, the area of the control capacitor is controlled by the area of the subpixel, so that it is realistically difficult to set the capacitance of the control capacitor at a preferable level. Since the structure shown in Fig. 11 (b) can be designed more freely than the structure having an undivided pixel electrode, the former is

more preferable than the latter.

However, several problems regarding the structure shown in Fig. 11 (b) have been found. First, in order to achieve this structure, control capacitor electrode 115 and insulating film 114 have to be additionally formed, thus increasing manufacturing cost. In addition, by utilizing white and black gradations that are independent of viewing angle, a plurality of subpixels of one pixel is activated at these gradations, and a gradation display (gray scale display) that is independent of viewing angle is provided by the number of subpixels displaying white or black gradations, so that the number of subpixels has to be increased with the addition of gradation to a display. As a result, the driving voltage increases. The third problem of the liquid crystal unit having a subdivided pixel electrode in each subpixel element is that sections formed with no electrodes - such as gaps between subpixels - increase, thus reducing the pixel display area and lowering the brightness of a display screen, lowering contrast due to the leakage of light from the gaps between the subpixels, and slowing the speed of response because of deformed liquid crystal molecular orientation caused by an electric field, spread in a horizontal direction at the edges of subpixel electrodes.

In order to solve the above-noted third problems, U.S. Pat. No. 5,245,450 discloses a liquid crystal display unit shown in Fig. 12 (second conventional example). A control capacitor electrode 126 below an insulating film 125 and on a bottom substrate 127 in the figure is made of a transparent conductive film, and is also formed at gaps between four subpixels 124. A common electrode 122 is formed on the interior surface of a top substrate 121, and a liquid crystal layer 123 is sandwiched between two substrates. The problem mentioned above is solved by activating liquid crystal layer 123 with voltage applied between control capacitor electrode 126 and control electrode 122. However, compared with other conventional TFT liquid crystal display units, the unit is produced at a low rate since the transparent conductive film and insulating film 125 have to be additionally formed.

Furthermore, a display unit disclosed in Published Unexamined (Laid-Open) Japanese Patent Application No. Hei 5-289108 (third conventional example) also solves the above-described third problem. According to the plan view shown in Fig. 13, the gap between subpixel electrodes 133 and 134 and the peripheral section of subpixels are covered with a control capacitor electrode 135; a shielding electrode made of chrome or the like. In the figure, 131 is a gate wiring, and 132 is a source wiring. In this conventional example, control capacitor electrode 135 can be formed at the same time when a chrome film is formed as a shielding section 136 for a TFT section, so that the manufacturing processes are simple. Contrast is also at a preferable level since light does not

leak from the gap between subpixel electrodes 133 and 134. However, the brightness of the display decreases due to the complete shielding of light at the gap between subpixel electrodes 133 and 134.

It is an object of the present invention to provide an improved liquid crystal display unit by a conventional manufacturing method that has superior gradation properties over a wide viewing angle, thus preventing an increase in manufacturing cost.

In order to accomplish this object, the liquid crystal display unit of the present invention includes a thin-film transistor of a reverse staggered structure, a plurality of subpixels in each pixel, and a control capacitor coupled in series with a liquid crystal capacitor at one or more subpixels. The control capacitor consists of a conducting pattern, which is formed at the same time as a source and a drain electrodes constituting the thin-film transistor, and subpixel electrodes.

It is a feature of the present invention to provide a liquid crystal display unit having at least two subpixels, so that superior multiple gradation display with low voltage is achieved in a main viewing angle.

In order to accomplish this object, the liquid crystal display unit includes liquid crystal sandwiched between two substrates, pixels arranged on the substrate in a matrix, at least two subpixels constituting each pixel, and a means to apply different voltages to a liquid crystal layer of each subpixel. The ratio of display area between the subpixels and the difference in driving voltage of the subpixels are controlled so as to provide a smooth curve describing or increasing a correlation between brightness and voltage of one pixel at an angle of 0-40° inclined from an axis perpendicular to the substrates along the long axis of liquid crystal molecules positioned in the middle layer of the liquid crystal.

It is yet another feature of the present invention to provide a bright liquid crystal display unit that does not shield gaps between pixels, but rather improves display properties by preventing the decrease in contrast caused by the leakage of light from subpixel gaps and the decline in speed of response due to the transitional disarray of liquid crystal orientation.

In order to accomplish this object, $0^\circ \leq \phi \leq 45^\circ$ when ϕ is an angle where the boundary of the subpixels intersects with the long axis of liquid crystal molecules in the middle of a liquid crystal layer that is determined by the directions of rubbing treatments on the top and bottom substrates. The gap between the subpixels is two times as thick or less than the liquid crystal layer.

The present invention will be described by referring to the following illustrative examples and attached figures.

Fig. 1 (a) is a plan view of a liquid crystal display unit having a thin-film transistor (TFT) of a first embodiment of the present invention.

Fig. 1 (b) is a cross-sectional view of the liquid crystal display unit of the first embodiment of the present invention.

Fig. 1 (c) is an equivalent circuit diagram of the liquid crystal display unit of the first embodiment of the present invention.

Fig. 2 is a plan view of a liquid crystal display unit having a TFT of a second embodiment of the present invention.

Fig. 3 is a graph showing the correlation between the quantity of light and signaling voltage of a pixel of the first embodiment of the present invention when the pixel is observed from the front.

Fig. 4 (a) is a graph showing the correlation between the quantity of light and signaling voltage of the pixel of the first embodiment of the present invention when a viewpoint is inclined to the direction of a main viewing angle by 40°.

Fig. 4 (b) is a graph showing the correlation of Fig. 4 (a), but on a smaller scale with regard to quantity of light.

Fig. 5 (a) is a graph showing the correlation between brightness and signaling voltage of the liquid crystal display unit of the first embodiment of the present invention when a viewpoint is inclined to the direction of a main viewing angle by 0-60°.

Fig. 5 (b) is a graph showing the correlation between brightness and signaling voltage of a liquid crystal display unit of a conventional example when a viewpoint is inclined to the direction of a main viewing angle by 0-60°.

Fig. 6 (a) illustrates rubbing directions on two transparent substrates of the liquid crystal display unit of the first embodiment of the present invention.

Fig. 6 (b) illustrates the orientation of a liquid crystal molecule influenced by the rubbing directions shown in Fig. 6 (a).

Fig. 6 (c) illustrates a boundary between the subpixels of the liquid crystal display unit of a third conventional example.

Fig. 6 (d) illustrates a boundary between the subpixels of the liquid crystal display unit of the first embodiment of the present invention.

Fig. 7 is a graph illustrating the properties of light leakage between the subpixels of liquid crystal display unit of the first embodiment of the present invention.

Fig. 8 (a) is a cross-sectional view taken on line b-b' of Fig. 1 (a), illustrating the movement of liquid crystal molecules in the middle layer of the liquid crystal between subpixels when the liquid crystal display unit of the first embodiment of the present invention shifts from white to black scale gradations.

Fig. 8 (b) is a plan view taken on line b-b' of Fig. 1 (a), illustrating the movement of liquid crystal molecules in the middle layer of the liquid crystal between subpixels when the liquid crystal display unit of the first embodiment of the present invention shifts from

white to black scale gradations.

Fig. 9 (a) is a cross-sectional view taken on line c-c' of Fig. 13, illustrating the movement of liquid crystal molecules in the middle layer of the liquid crystal between subpixels when the liquid crystal display unit of the conventional example shifts from white to black scale gradations.

Fig. 9 (b) is a plan view taken on line c-c' of Fig. 13, illustrating the movement of liquid crystal molecules in the middle layer of the liquid crystal between subpixels when the liquid crystal display unit of the conventional example shifts from white to black scale gradations.

Fig. 10 is a plan view of a conventional TFT liquid crystal display unit.

Fig. 11 (a) is an equivalent circuit diagram of the liquid crystal display unit of a first conventional example.

Fig. 11 (b) is a cross-sectional view of the liquid crystal display unit of the first conventional example.

Fig. 12 is a perspective view of the liquid crystal display unit of a second conventional example.

Fig. 13 is a plan view of the liquid crystal display unit of the third conventional example.

Fig. 14 (a) illustrates a viewpoint when the viewing angle properties of the liquid crystal display unit of the present invention are measured.

Fig. 14 (b) is a plan view of two transparent substrates, illustrating the directions of orienting the substrates of the present invention.

Example 1

Fig. 1 (a) is a plan view of a TFT liquid crystal display unit of the present invention. Fig. 1 (b) is a cross-sectional view taken on line a-a' of Fig. 1 (a). Fig. 1 (c) is an equivalent circuit diagram of one pixel of the liquid crystal display unit. The method of manufacturing a TFT array substrate includes the following steps. Forming transparent electrodes 2a and 2b on a transparent glass substrate 1a for driving the liquid crystal of subpixels 21a and 21b. Forming a silicon oxide film 3 on the substrate and the electrodes as an insulating film. Forming a TFT gate electrode 4 made of metal such as chrome. Laminating a silicon nitride film 5 on the gate electrode as a TFT gate insulating film. Forming a semiconductor layer 6 comprising TFT on the gate insulating film. Making holes 7a and 7b in silicon oxide film 3 and silicon nitride film 5, thus exposing a section of transparent electrode 2a. Forming a source electrode 8a, a drain electrode 8b, and a control capacitor electrode 8c at the same time by using aluminum/titanium two-layer metal, thus coupling drain electrode 8b to transparent electrode 2a through hole 7a and control capacitor electrode 8c to transparent electrode 2a through hole 7b. Forming a control capacitor 23 between control capacitor electrode 8c and transparent electrode 2b. And, forming

an additional capacitor 22 between the gate electrode and control capacitor electrode 8c.

The resistance of the semiconductor layer changes due to voltage applied to gate electrode 4, and the layer has properties of a switch element. 24 is a thin-film transistor (TFT). The TFT array substrate and another substrate 1b formed with black matrices 9 and a transparent electrode 10 are coated with alignment films and are provided with a rubbing treatment or the like. Then, these two substrates are combined with an about 5µm gap in-between, and liquid crystal 11 is injected in the gap. Furthermore, two polarization plates are arranged on the exterior surfaces of the two substrates so as to set the polarization axis of one plate at right angles to that of another plate. As a result, the liquid crystal display unit of the first embodiment is manufactured. Referring to Fig. 1 (c), driving voltage (Vd) from the TFT is supplied to the liquid crystal layer, and voltage applied to subpixel 21a is Vlc1. Control capacitor 22 (Cc) is coupled in series with the liquid crystal capacitor of subpixel 21b between the TFT drain electrodes and the transparent electrode. The driving voltage (Vd) supplied from the TFT is applied to control capacitor 22 and the liquid crystal capacitor (Clc2), and the voltage applied to the liquid crystal of subpixel 21b is lower than that applied to the liquid crystal of subpixel 21a, as shown in the following formula:

$$\begin{aligned} V_{lc1} &= V_d, \\ V_{lc2} &= V_d \times (C_c / (C_{lc2} + C_c)), \text{ thus} \\ V_{lc2} &< V_{lc1}. \end{aligned}$$

Fig. 10 is a plan view of a conventional TFT liquid crystal display unit in which pixels are not subdivided. No conductive material and insulating film are added to the liquid crystal display unit shown in Fig. 10 when manufacturing the display unit shown in Fig. 1 (a). Since the control capacitor is formed at the same time as the source and the drain electrodes, the display unit of the first embodiment can be manufactured by the conventional manufacturing method. Thus, a liquid crystal display unit having superior gradation display over a wide viewing angle can be provided for only the same cost required for manufacturing the conventional display unit.

Referring to Fig. 1 (b), control capacitor electrode 8c is electrically connected to drain electrode 8b, thus reducing the area occupied by the electrodes constituting the control capacitor compared with the area required for the electrodes of the conventional display unit (third conventional example) shown in Fig. 13. In this conventional display unit, the control capacitor consists of two capacitors coupled in series - a first subpixel electrode 133/control capacitor electrode 135 and a control capacitor electrode 135/second subpixel electrode 134. When the capacitance of the two capacitors is the same, each capacitor has to be twice as large as the capacitor applied to the display unit of the first embodiment in order to obtain the

same capacitance that obtained in the unit of the first embodiment. In addition, the area occupied by the control capacitor is four times more than the area required for the capacitor in the display unit of the first embodiment since the capacitor in the conventional display unit employs two electrostatic capacitors. In the conventional display unit, the step of forming a hole in the insulating film so as to couple first subpixel electrode/drain electrode 133 to the control capacitor electrode is required, but is not needed in manufacturing the display unit of the first embodiment. Since the control capacitor electrode of the conventional display unit is made of a transparent conductive material, the transmissivity of light lowers, thus reducing the brightness of the display unit viewed from the front. More specifically, when color 10.4-type 640 (one pixel with three colors - R, G and B) \times 480 pixels are used for a conventional liquid crystal display unit, the numerical aperture (ratio between the area of transmitted light per pixel and the entire area of the pixel) is about 50%. On the other hand, the area occupied with the control capacitor is about 10% of the entire pixel area and the numerical aperture is 40%, when the above-mentioned pixels are applied to the display unit of the third conventional embodiment. In other words, the brightness of the display unit (third conventional example) from the front is less than the first conventional display unit (Fig. 10) by about 20%. However, in the display unit of the first embodiment of the present invention, the area occupied with the control capacitor is about 30% of the entire pixel area, and the numerical aperture is about 47%. Thus, the brightness from the front is lower than that of the first conventional display unit mentioned above only by about 6%, an extremely small reduction.

Fig. 5 (b) is a graph illustrating the correlation between brightness and signaling voltage observed from a main viewing angle. The main viewing angle is defined as the direction of a viewpoint 38 shifted to the right as shown in Fig. 14 (a), which is a cross-sectional view taken on line a-a' of Fig. 14 (b). Referring to Fig. 14 (b), the liquid crystal is sandwiched between two transparent glass substrates 1a and 1b, and bottom substrate 1a and top substrate 1b are oriented in directions 30a and 30b respectively. In Fig. 14 (a), the liquid crystal molecules 35 are upright because of voltage applied to the section between the two substrates. In addition, the viewing angle (θ) is defined as an angle inclined from an axis perpendicular to the substrates. Referring to Fig. 5 (b), in a conventional liquid crystal display unit, the gradation display becomes poor as a viewpoint is shifted to the main viewing angle. This is because the additional peaks of brightness appear on a high voltage side (called gradation reversal). In gradation reversal, the brightness at a white scale gradation and at a black scale gradation reverses, presenting a seemingly negative picture image.

Fig. 3 is a graph illustrating the correlation between the quantity of light and signaling voltage of a pixel when the liquid crystal display unit of the first embodiment is observed from the front. Control capacitor Cc is controlled to set Cc:Clc2 (V50) equal to 9:5. The capacitance of liquid crystal capacitor Clc2 changes depending on voltage applied to the capacitor and the direction of orienting the liquid crystal molecules. Clc2 (V50) is the capacitance when the quantity of light from the subpixel is 50% of the maximum quantity of light. The maximum quantity of light is obtained when no voltage is applied. The liquid crystal molecules are arranged almost parallel to the substrates with the maximum quantity of light. Referring to Fig. 3, when the slope of a line indicating the correlation between the quantity of light and signaling voltage of subpixel 1 is γ and the difference in driving voltage between subpixel 1 and subpixel 2 is ΔV , the following formulas are obtained:

$$\begin{aligned}\gamma &= \left| \frac{V10 - V90}{V50' - V50} \right| = 1.3V, \\ \Delta V &= \left| V50' - V50 \right| = 1.0V, \text{ thus} \\ \gamma - \Delta V &= 0.3V.\end{aligned}$$

In the formulas, V90, V50 and V10 are signaling voltages when the quantities of light of subpixel 1 are 90%, 50% and 10% of the maximum quantity of light, respectively. Similarly, V50' signaling voltage is obtained when the quantity of light of subpixel 2 is 50% of the maximum quantity of light.

The ratio between display area 26a of subpixel 1 and display area 26b of subpixel 2 shown in Fig. 1 (a) is 7:3. Thus, as shown in Figs. 4 (a) and (b), curves indicating the quantity of light and signaling voltage properties of a pixel, which is the combination of the properties of subpixels 1 and 2, are smooth. Moreover, the quantity of light decreases smoothly as signaling voltage increases. The gradation reversal of the conventional display unit shown in Fig. 5 (b) is removed at any viewing angle when the display unit of the first embodiment is applied. (See Fig. 5 (a).)

When $\gamma - \Delta V$ is more than -0.2V and less than 0.8V and the ratio between the display area of subpixel 1 and that of subpixel 2 is within the range of 8:2 to 6:2, a curve indicating the quantity of light and signaling voltage properties of a pixel is smooth. In addition, the quantity of light decreases smoothly as signaling voltage increases.

Therefore, by controlling the ratio of display area and the difference in driving voltage between subpixels, the gradation reversal observed from the main viewing angle is removed and good multiple gradation display properties are obtained even in a liquid crystal display unit having only two subpixels for each pixel.

Fig. 6 (a) illustrates rubbing directions 31 on the bottom substrates 1a of the liquid crystal display unit of the first embodiment. Fig. 6 (a) illustrates rubbing directions 32 on the top substrates 1b of the liquid crystal display unit of the first embodiment. Fig. 6 (b) illustrates the long axis direction 33 of the liquid crys-

tal molecule 34 in the middle layer of the liquid crystal layer. Fig. 6 (c) illustrates a boundary between subpixels in the conventional liquid crystal display unit (third conventional example). Fig. 6 (d) illustrates a boundary between subpixels in the liquid crystal display unit of the first embodiment. Fig. 9 (a) is a cross-sectional view taken on line c-c' of Fig. 13; Fig. 9 (b) is a plan view taken on line c-c' of Fig. 13. Figs. 9 (a) and (b) also illustrate the movement of liquid crystal molecules when display is shifted from the white scale to the black scale. Referring to Fig. 9 (a), (1) shows the liquid crystal molecules 35 in a steady state at the white scale; (4) illustrates the molecules in a steady state at the black scale; (2) and (3) show the molecules in transitional states from the white to the black scales. At the white scale (1), the liquid crystal molecules 35 are oriented with an angle of several degrees relative to the substrates (tilt). When applied voltage is switched to the black scale, the molecules stand with larger tilts due to an electric field perpendicular to the substrates and between the common electrode 10 and each of subpixel electrodes 21a and 21b. However, at the boundary between subpixel electrodes, the directions of electric field incline (indicated as slightly inclined dotted lines in (2)), thus influencing the orientation of liquid crystal molecules above the boundary. In this example, the long axes of liquid crystal molecules are almost parallel to the directions of the electric field at the boundary, so that the molecules are likely to be influenced by the direction of electric field. As a result, as shown in (2) and (3), some liquid crystal molecules are oriented differently from other molecules and along the direction of electric field. The leakage of light 37a-37d are generated at the boundary between liquid crystal molecules with different orientations (domain). As shown in (4), the orientation of some liquid crystal molecules becomes normal at the end due to the effect of other molecules which are already oriented regularly. However, compared with a display unit in which pixels are not subdivided, this conventional display unit requires much time to shift from the white scale to the black scale. As a result, an after-image appears on the displayed image. This problem can be solved when the boundary between subpixels is shielded with opaque patterns as shown in Fig. 13. However, due to the opaque patterns, the numerical aperture of pixels decline, and the brightness of display decreases.

Fig. 8 (a) is a cross-sectional view taken on line b-b' of Fig. 1 (a); Fig. 8 (b) is a plan view taken on line b-b' of Fig. 1 (a). The figures illustrate the movements of liquid crystal molecules 35 in the middle layer of the liquid crystal layer when the white scale is shifted to the black scale. 10 is a common electrode. At the boundary between the subpixel electrodes 21a, 22b, the directions of the electric field 36 incline (indicated as slightly inclined dotted lines in (2)), so that the orientation of liquid crystal molecules above the bound-

ary is influenced. However, in this example, the long axes of liquid crystal molecules are perpendicular to the directions of the electric field, so that the molecules are rarely affected by the electric field. Therefore, the movement of molecules above the boundary is the same as that of the molecules above the electrodes, and no irregular orientation mentioned above is found in this example. In other words, it is not necessary to form a shielding pattern at the boundary of subpixels; thus, the numerical aperture of the pixel and the brightness of the display screen are maintained.

Referring to Fig. 1 (b), the gap between the subpixel electrodes (w) is twice as thick as the liquid crystal layer (d) or less. Voltage applied to the liquid layer of subpixel 2 is a divided voltage, which is applied to subpixel 1 as shown in Fig. 1 (c), and the subpixels have the same polarity. About 60% of signaling voltage is applied to subpixel 2. When the saturation voltage of the liquid crystal is 6V and signaling voltage is 10V, voltage applied to subpixel 1 is 10V and that to subpixel 2 is 6V so as to obtain a preferable black scale. The potential difference between subpixel electrode 1 and subpixel electrode 2 is 4V at maximum, and is much smaller than voltage generated between subpixel electrode 1 and source electrode 8a, and between subpixel electrode 2 and source electrode 8a. The potential difference is also slightly smaller than the voltage applied between subpixel electrode 2 and common electrode 10. In other words, the generation of an electric field between the subpixels is weak.

By changing the width of the gap between subpixel electrode 1 and subpixel electrode 2 (w) and the ratio of voltage applied to subpixel 2 relative to signaling voltage, the leakage of light from the gap and from the peripheral section of the subpixels was tested, thus obtaining the graph shown in Fig. 7. The gap width (w) is normalized by the thickness of liquid crystal layer (d). The capacitance of the liquid crystal changes, depending on applied voltage. The ratio of voltage mentioned above is obtained by applying the capacitance when signaling voltage is high and the liquid crystal is activated. When the ratio is in the range of 10% or less, signaling voltage required to operate subpixel 2 becomes too high, so that tests were not carried out in that range. Referring to Fig. 7, it is found that light is less likely to leak as the gap width (w) becomes small relative to the thickness of liquid crystal layer (d) and the ratio of voltage becomes large.

There are two reasons why it is preferable to have a narrower gap between the subpixel electrodes. First, the liquid crystal molecules above the gap move along with the discharge of electric field and the movement of liquid crystal molecules above the subpixels. When signaling voltage is high, the liquid crystal molecules above the gap and near the subpixels

move due to the first reason mentioned above, thus reducing brightness. However, when the gap width is small, all the liquid crystal molecules above the gap respond to signaling voltage, thus providing superior black scale gradation. Secondly, the irregular orientation of liquid crystal molecules is unlikely to occur when the gap is small, thus preventing the scatter and leakage of light. When the gap is small, it is believed that the spread of electric field is restricted even though the intensity of the field increases, thus reducing the negative effect on liquid crystal molecules above the gap. The reason why leakage of light is unlikely to occur when the ratio of voltage applied to subpixel 2 relative to signaling voltage is high is that the potential difference between the subpixel electrodes becomes small.

Referring to Fig. 7, leakage of light due to the irregular orientation of liquid crystal molecules or the like is not a problem in the region identified as "most preferable", and the black scale gradation is in extremely good condition since the liquid crystal molecules above the gap move according to signaling voltage. Thus, a layer to shield the gap is not required. In the region identified as "preferable" shown in Fig. 7, the gap does not become completely black under ordinary driving conditions and signaling voltage. However, leaked light due to irregular orientation or the like does not enter the subpixels, so that the display is practical enough even without the shielding layer, as long as extremely high contrast is not required. In the region identified as "large leakage of light", leaked light enters the subpixels, so that display becomes poor with no shielding layer.

When the gap width between the subpixels is set to be two times as large as the thickness of the liquid crystal layer or less, the display properties of the display unit are "most preferable" or "preferable" with any ratio of voltage applied to subpixel 2 relative to signaling voltage. Even though it is preferable to set the voltage ratio at 40-80%, "most preferable" display properties are likely to be obtained at almost any voltage ratio as long as the gap width is two times as thick as the liquid crystal layer or less. Taking manufacturing cost into consideration, the gap width between the subpixels is preferably 1.5 time as thick as the liquid crystal layer or less.

In consideration of processing cost and the leakage of light, a several to 10 μm -thick shielding layer should be laminated on each subpixel and the gap between the subpixels. Thus, the thickness of the shielding layer becomes 20-30 μm . With no formation of the layer, light transmitted through the gap is utilized at the white scale gradation, so that the brightness of the liquid crystal unit improves. In a 640 \times 400 dot liquid crystal display unit with a 26cm diagonal line, the pixel pitch is about 300 μm . However, by applying the display unit of the present invention, the brightness at the white scale gradation increases by

about 10%.

Example 2

The second embodiment of the present invention is explained by referring to Fig. 2. In this example, a control capacitor electrode is made of a pattern 8b coupled to a drain electrode constituting a thin-film transistor. Other structures and effects of the display unit are the same as the ones of the display unit of the first embodiment of the present invention.

Claims

1. A liquid crystal display unit comprising:
two insulating substrates;
a liquid crystal layer sandwiched between said two insulating substrates;
a plurality of pixels arranged in a matrix;
at least two subpixels in each pixel;
a thin-film transistor of a reverse staggered structure for driving said subpixels; and
a control capacitor coupled in series with a liquid crystal capacitor of each subpixel at one or more subpixels;
wherein a pair of electrodes forming said control capacitor comprises a conducting pattern, which is formed at the same time as a source electrode and a drain electrode constituting said thin-film transistor, and a subpixel electrode.
2. The liquid crystal display unit of claim 1, wherein the conducting pattern is electrically coupled with the drain electrode.
3. A liquid crystal display unit comprising:
two insulating substrates;
a liquid crystal layer sandwiched between said two insulating substrates;
a plurality of pixels arranged in a matrix;
at least two subpixels in each pixel; and
means for applying voltage to a liquid crystal layer of each subpixel;
wherein voltage applied to a liquid crystal layer of one subpixel is different from voltage applied to a liquid crystal layer of another subpixel; and
wherein a ratio of display area between said subpixels and a difference in driving voltage of said subpixels are controlled so as to provide a smooth curve describing or increasing a correlation between quantity of light and signaling voltage for a pixel; said pixel being observed at an angle of 0-40° inclined from an axis perpendicular to said insulating substrates along a long axis of liquid crystal molecules positioned in a middle layer of said liquid crystal layer with no application of voltage.

4. The liquid crystal display unit of claim 3, wherein the pixel comprises two subpixels.

5. The liquid crystal display unit of claim 4, wherein the two subpixels have different driving voltages; wherein a difference in driving voltages (ΔV) being expressed as follows:

$$-0.2V < \gamma - \Delta V < 0.8V;$$

$$\gamma = |V_{10} - V_{90}|; \text{ and}$$

$$\Delta V = |V_{50'} - V_{50}|;$$

where V_{10} , V_{50} and V_{90} are signaling voltages so as to set quantities of light for one subpixel, which is driven applying high voltage, at 90%, 50% and 10% relative to a maximum quantity of light, and $V_{50'}$ is a signaling voltage so as to set a quantity of light of another subpixel, which is driven applying low voltage, at 50% relative to a maximum quantity of light.

6. The liquid crystal display unit of claim 5, wherein the two subpixels have from 8:2 to 6:4 ratios of display areas; said ratios of display areas being ratios of a display area of one of said subpixels relative to a display area of another subpixel.

7. A liquid crystal display unit comprising:
two insulating substrates;
a liquid crystal layer sandwiched between said two insulating substrates;
a polarizing plate on an exterior surface of each insulating substrate so as to set a polarizing axis of one polarizing plate at right angles to a polarizing axis of another polarizing plate;
a plurality of pixels arranged in a matrix;
at least two subpixels in each pixel; and
means for controlling voltage applied to each subpixel;
wherein voltage applied to one subpixel is different from voltage applied to another subpixel; and
wherein $0^\circ \leq \phi \leq 45^\circ$ when ϕ is an angle between a long axis of liquid crystal molecules in a middle layer of said liquid crystal layer and a boundary between said subpixels.

8. The liquid crystal display unit of claim 7, further comprising a thin-film transistor and a control capacitor on one of the insulating substrates.

9. A liquid crystal display unit comprising:
two insulating substrates;
a liquid crystal layer sandwiched between said two insulating substrates;
a polarizing plate on an exterior surface of each insulating substrate so as to set a polarizing axis of one insulating substrate at right angles to a polarizing axis of another insulating substrate;
a plurality of pixels arranged in a matrix;
at least two subpixels in each pixel; and

a means to control voltage applied to each subpixel;

wherein voltage applied to one subpixel is different from voltage applied to another subpixel; and wherein a gap between said subpixels is two times as thick as said liquid crystal layer or less.

10. The liquid crystal display unit of claim 9, wherein one of the two insulating substrates comprises a shielding layer on sections where the pixels are not formed.

11. The liquid crystal display unit of claim 10, further comprising a thin-film transistor and a control capacitor on one of the insulating substrates.

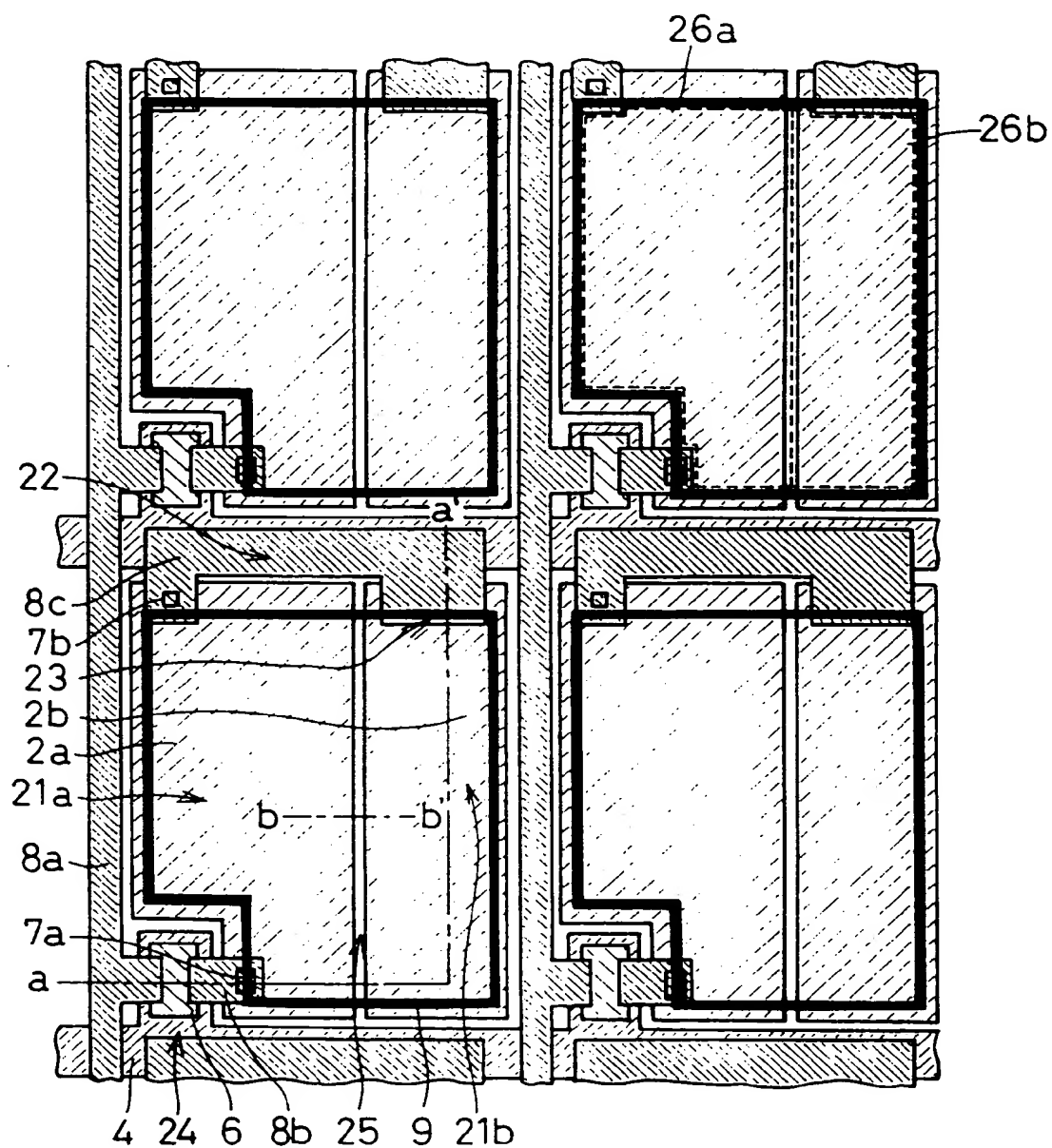


FIG.1 (a)

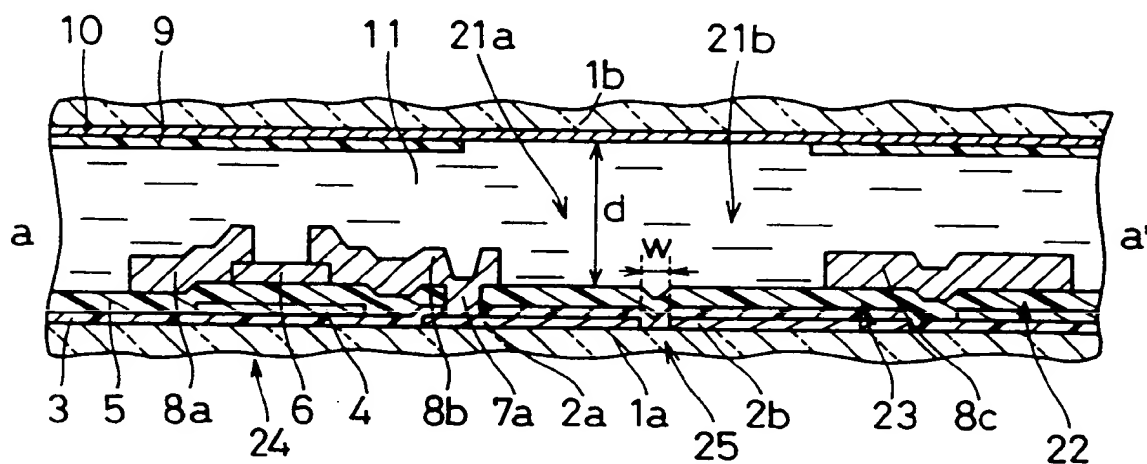


FIG. 1 (b)

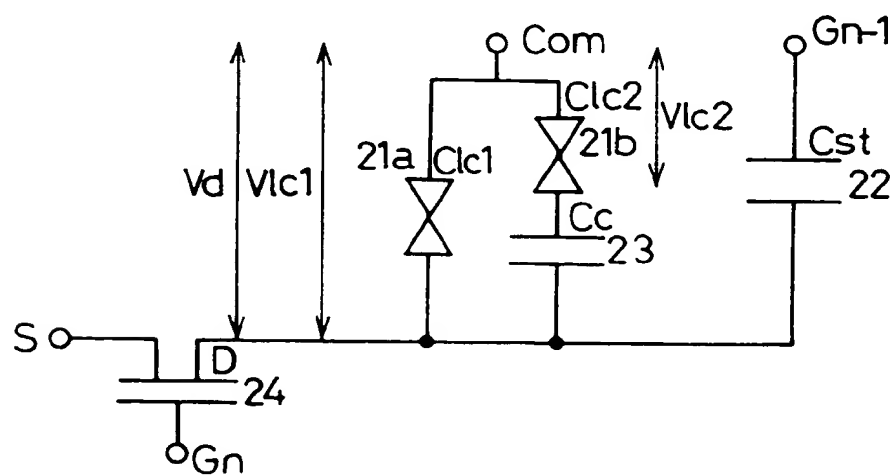


FIG. 1 (c)

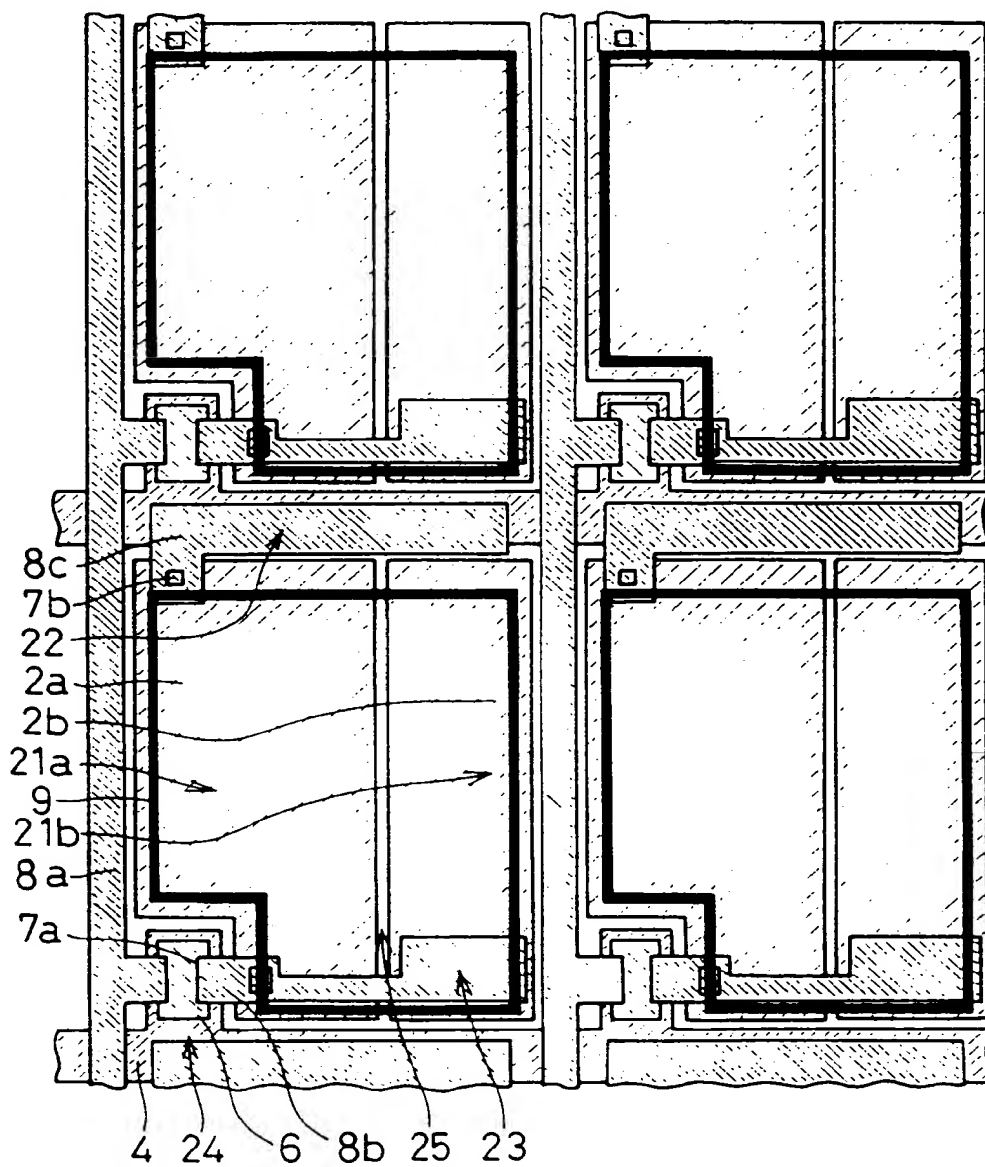


FIG. 2

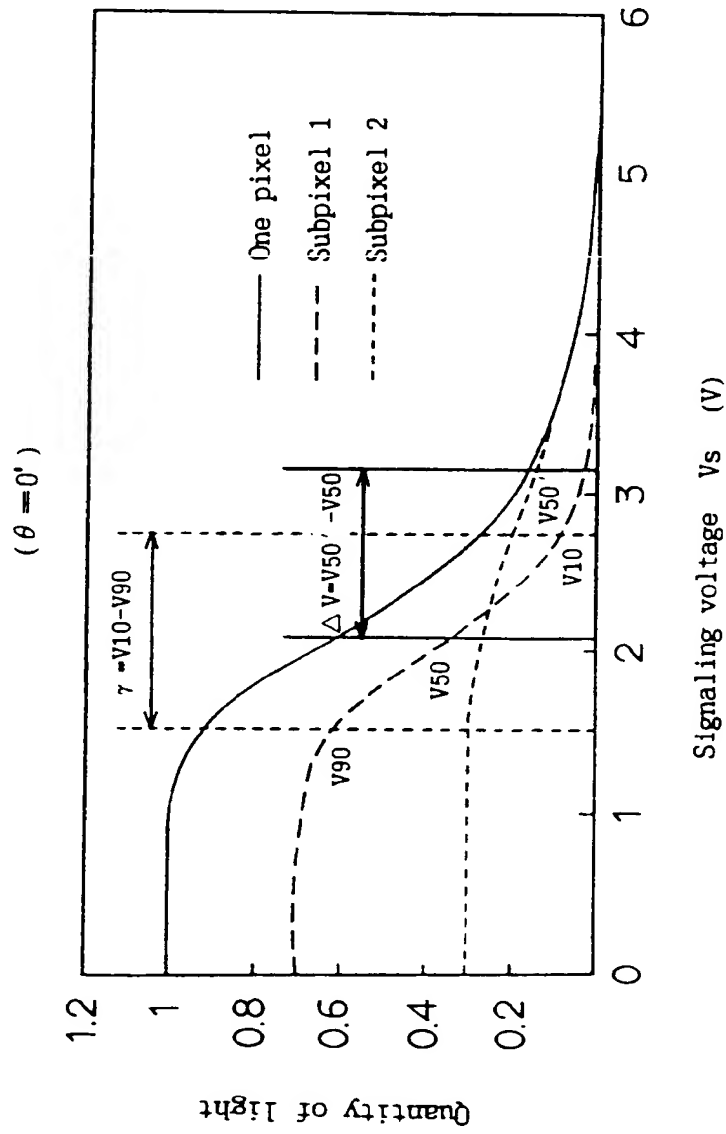


FIG. 3

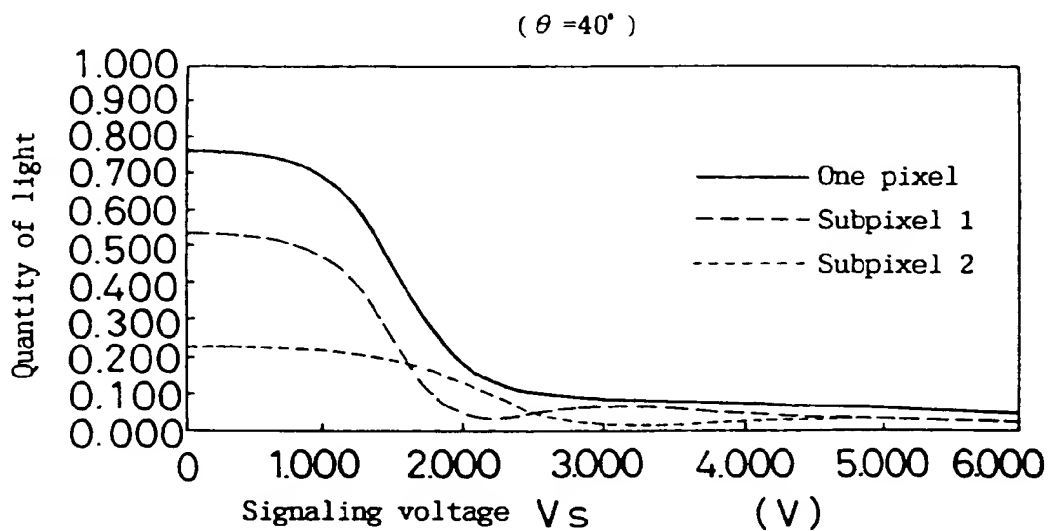


FIG. 4 (a)

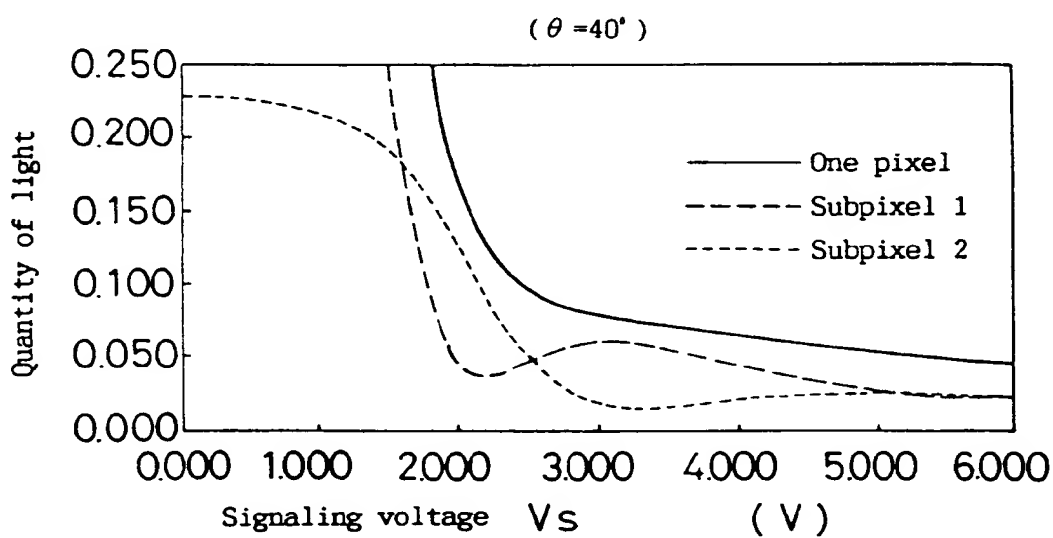


FIG. 4 (b)

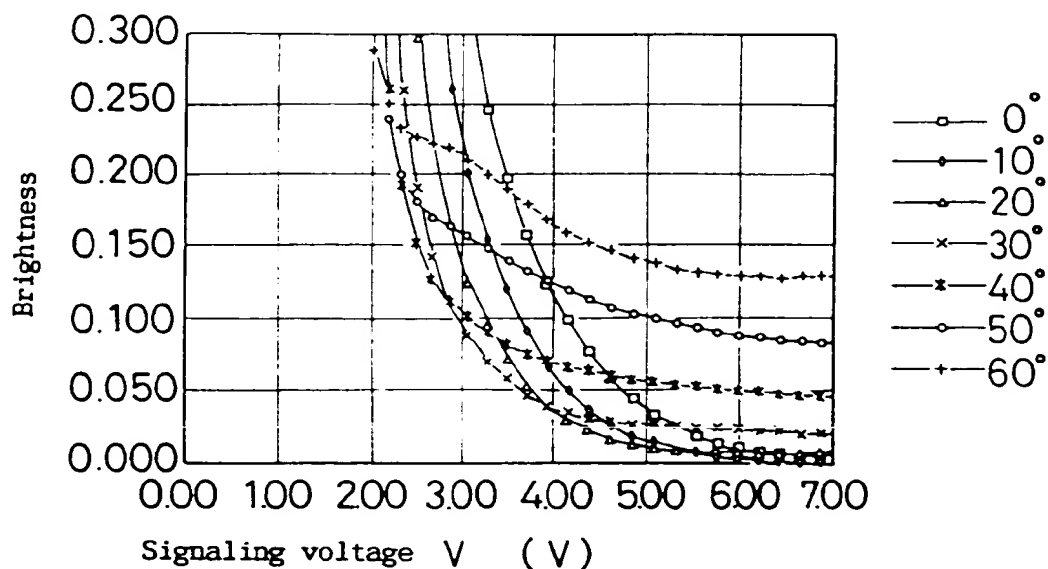


FIG. 5 (a)

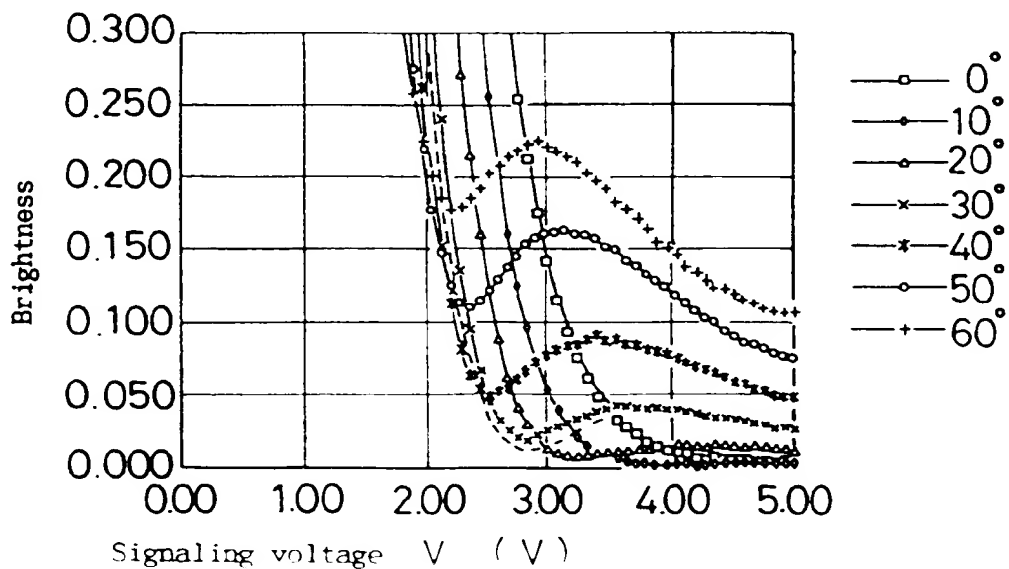


FIG. 5 (b)

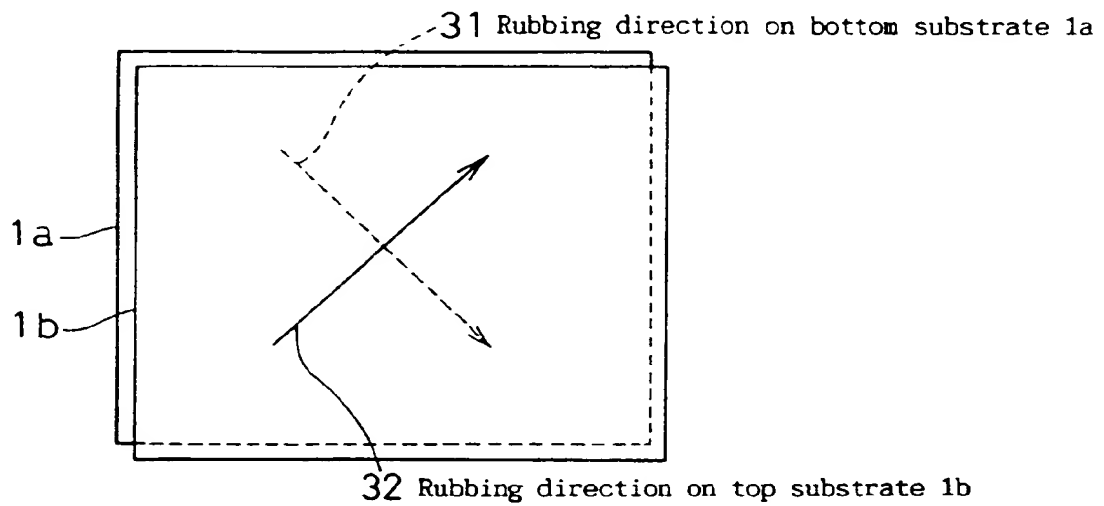


FIG.6 (a)

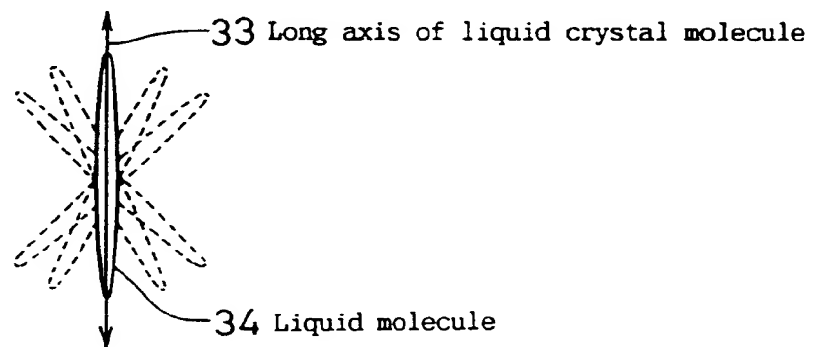


FIG.6 (b)

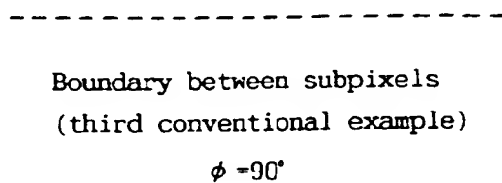


FIG.6(c)

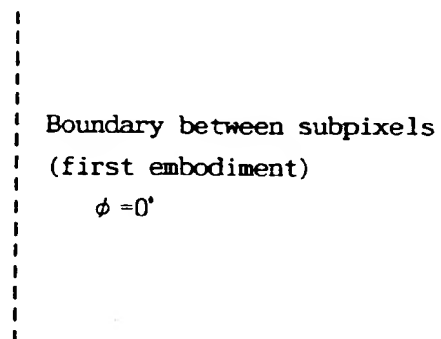


FIG.6(d)

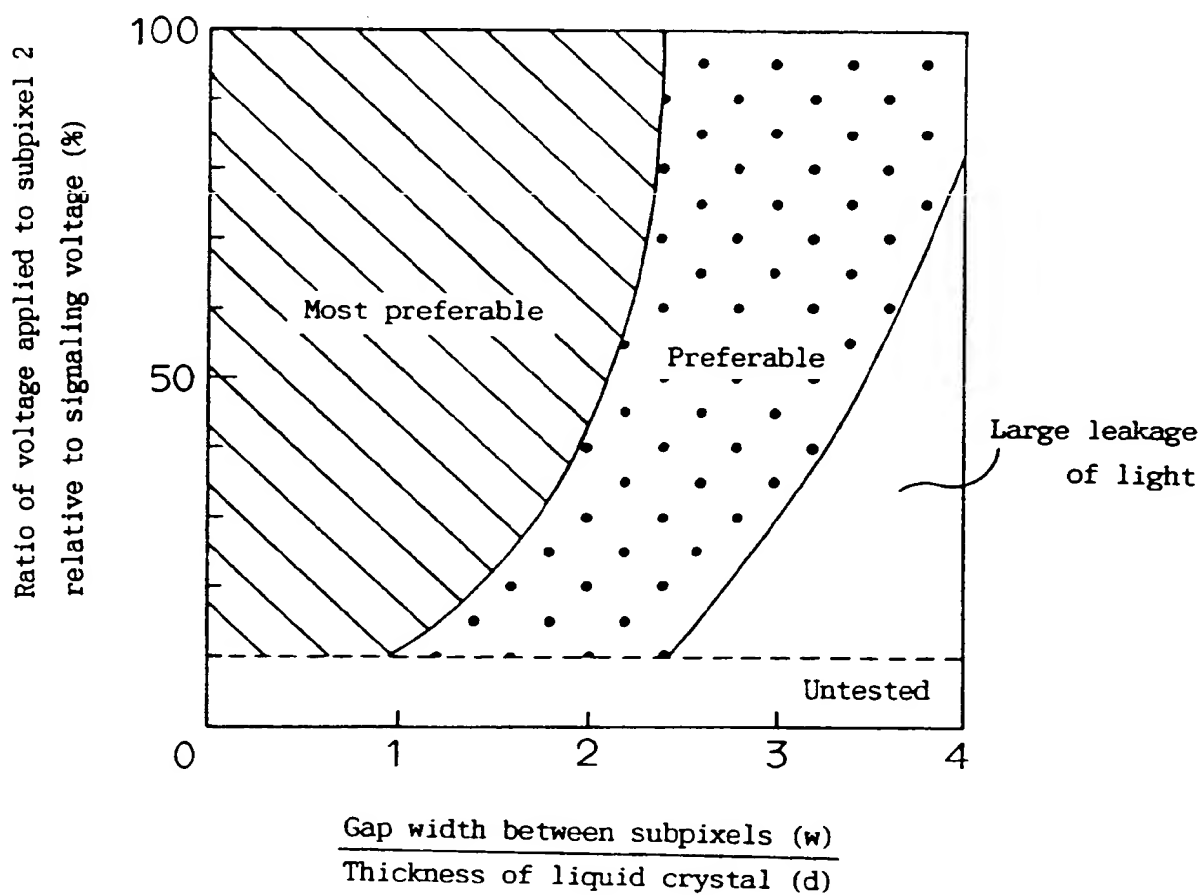


FIG. 7

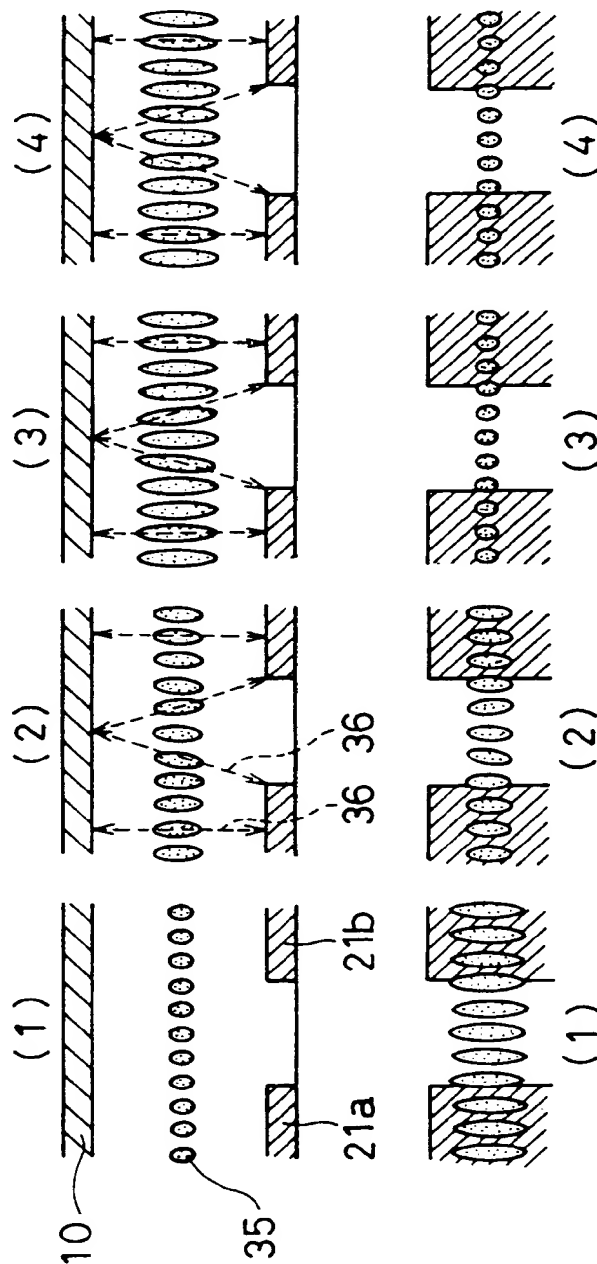


FIG. 8(a)

FIG. 8(b)

White scale gradation Black scale gradation

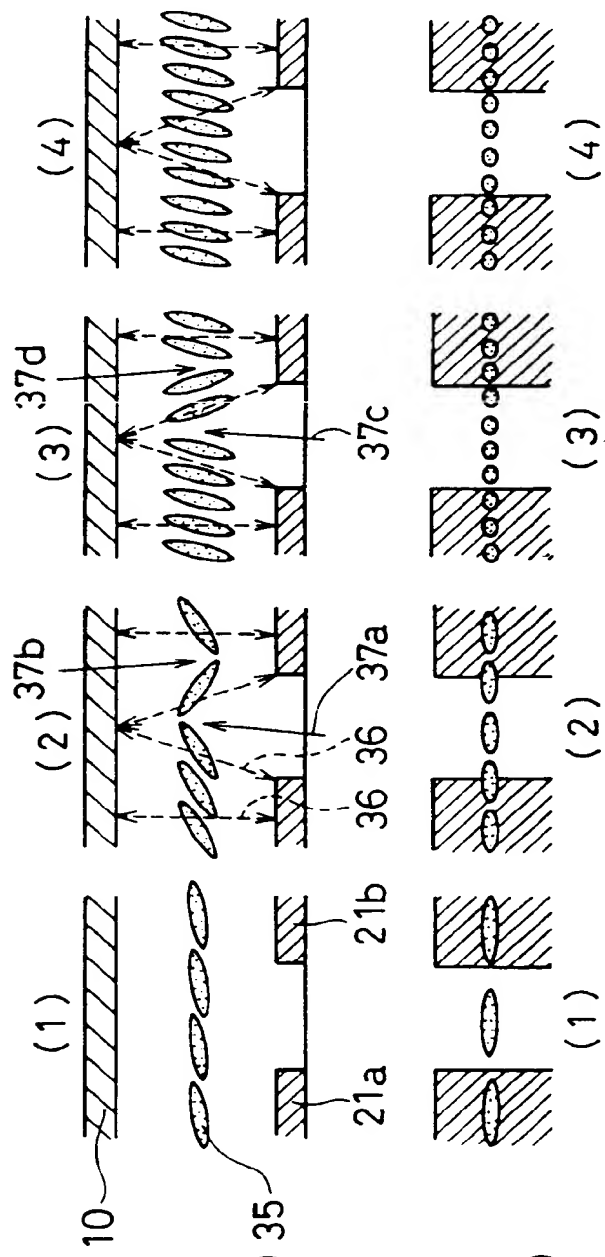


FIG 9 (a)
(PRIOR ART) 35

FIG 9(b)
(PRIOR ART)

White scale gradation → Black scale gradation

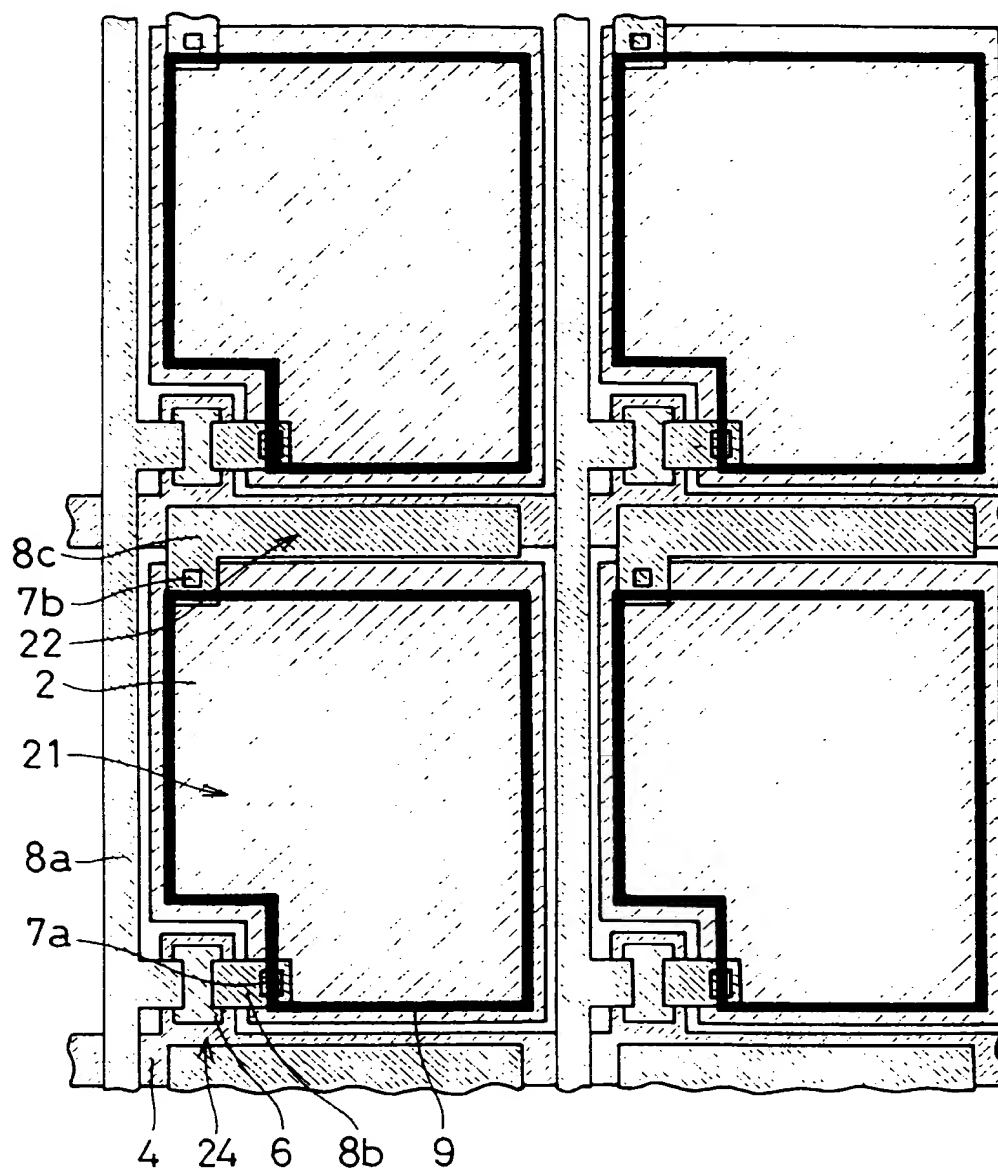


FIG.10
(PRIOR ART)

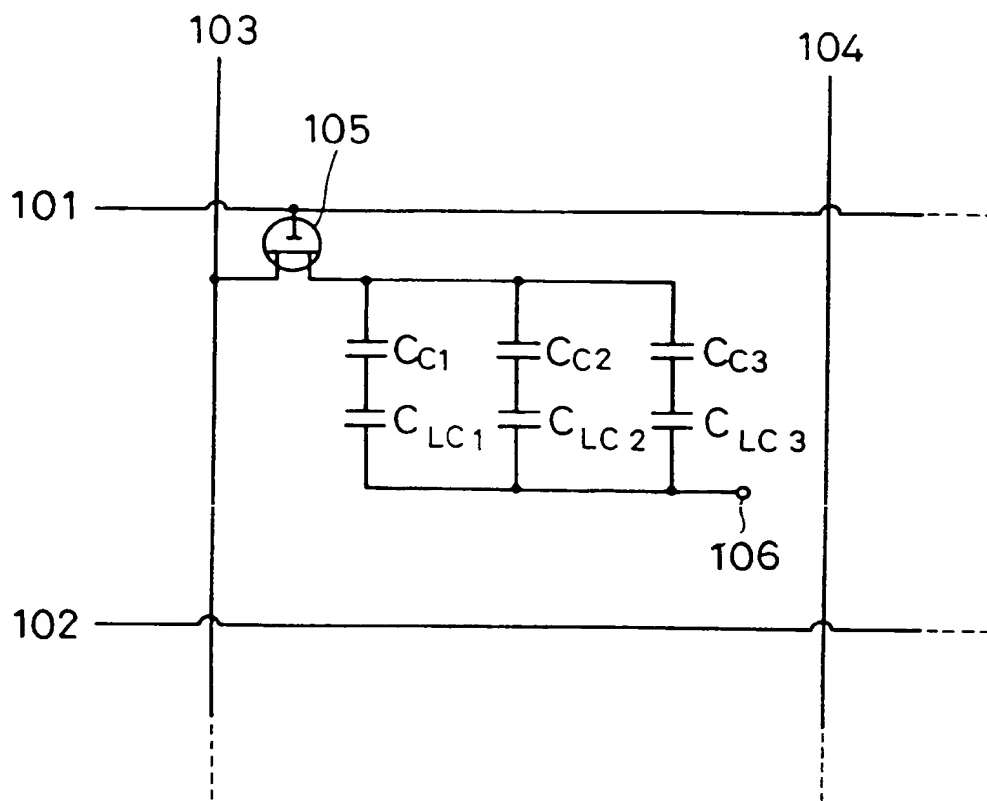


FIG. 11 (a)
(PRIOR ART)

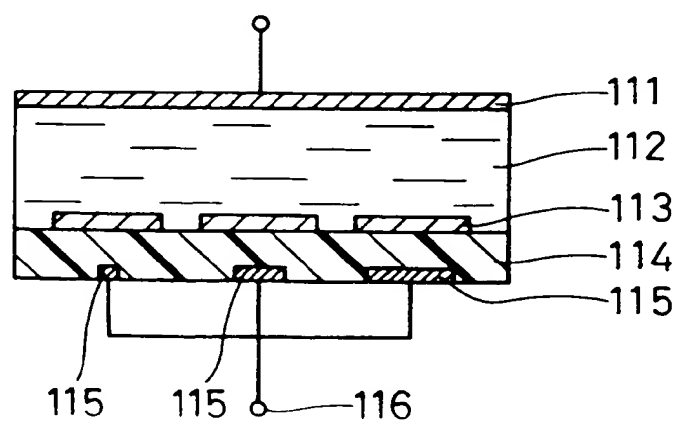


FIG. 11 (b)
(PRIOR ART)

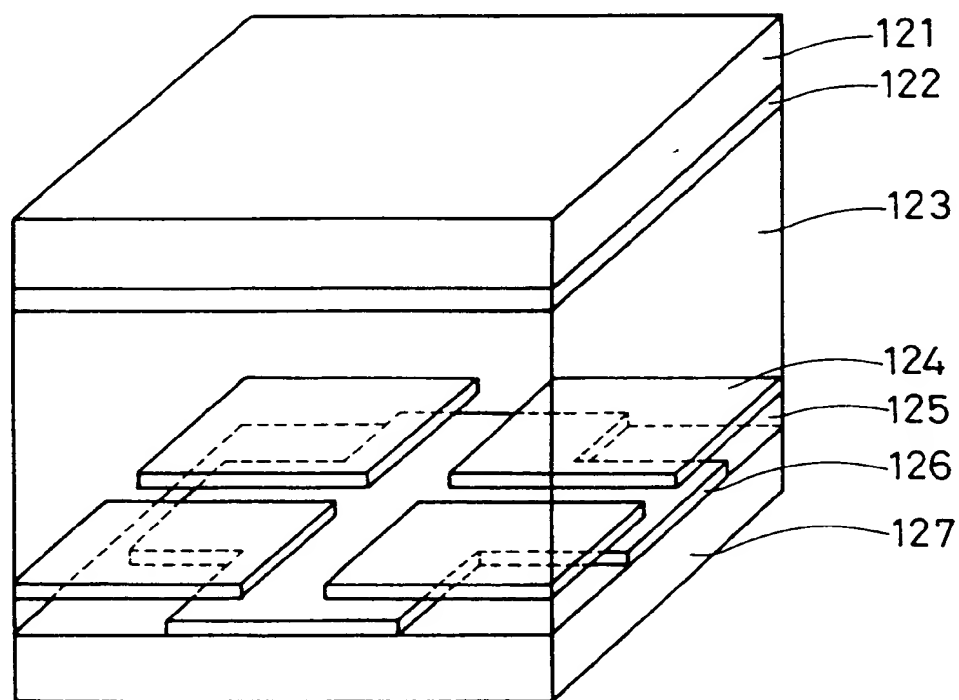


FIG.12
(PRIOR ART)

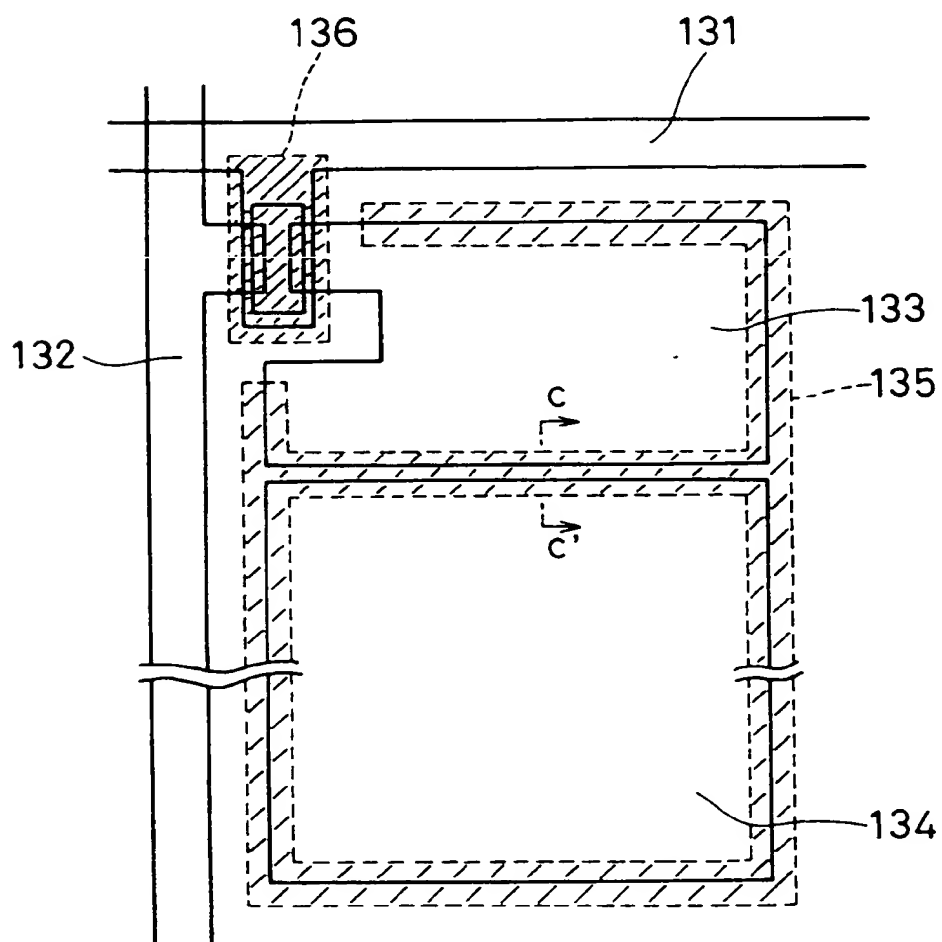


FIG. 13
(PRIOR ART)

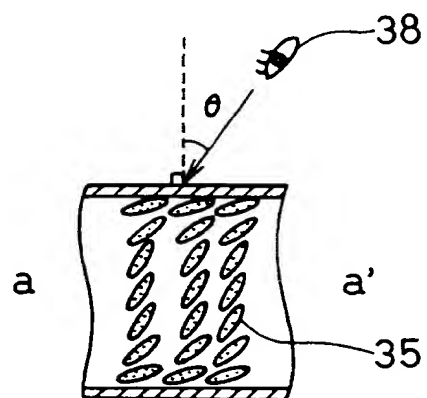


FIG. 14 (a)

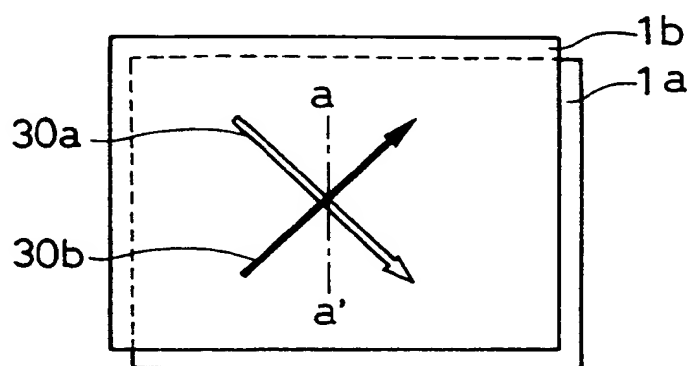
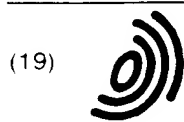


FIG. 14 (b)



(19)

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(11)

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(12)

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(54) Liquid crystal display unit with a plurality of subpixels

(57) A liquid crystal display unit including a plurality of subpixels (21a, 21b) in each pixel (21) provides superior gradation over a wide viewing angle, and is produced with no increase in manufacturing cost and no deterioration of desired properties (such as reduction in brightness, contrast and response). No additional manufacturing step is required since a control capacitor electrode (8c) is formed at the same time as source and drain electrodes (8a, 8b) constituting a thin-film transistor (24). By controlling the ratio of display area and the difference in applied voltage of the subpixels (21a, 21b), preferable viewing angle properties are obtained. The display properties at the boundary of subpixels are improved by controlling the gap width and the direction of boundary of the subpixels (25), thus providing a bright display.

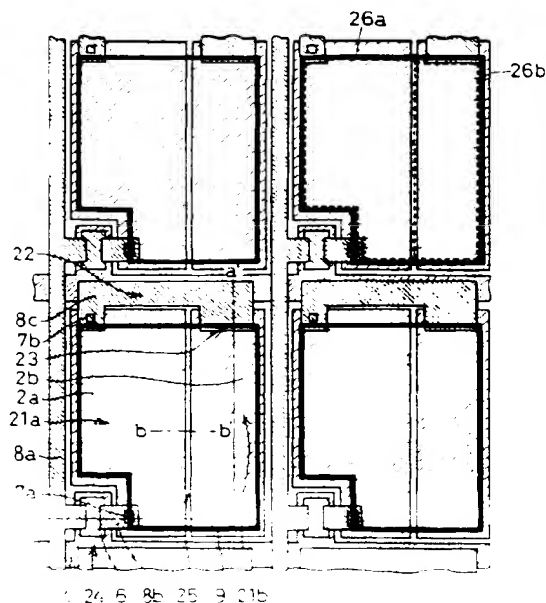


FIG 1 (a)

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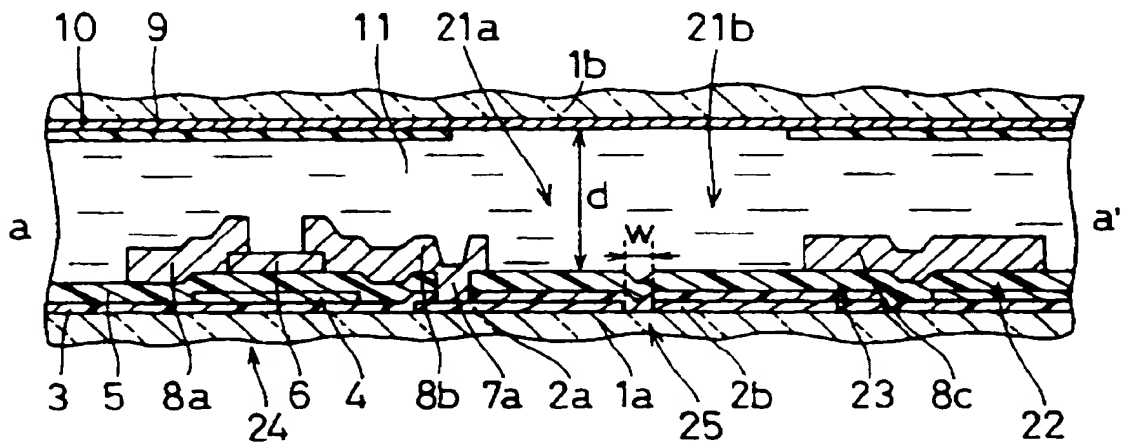


FIG. 1 (b)

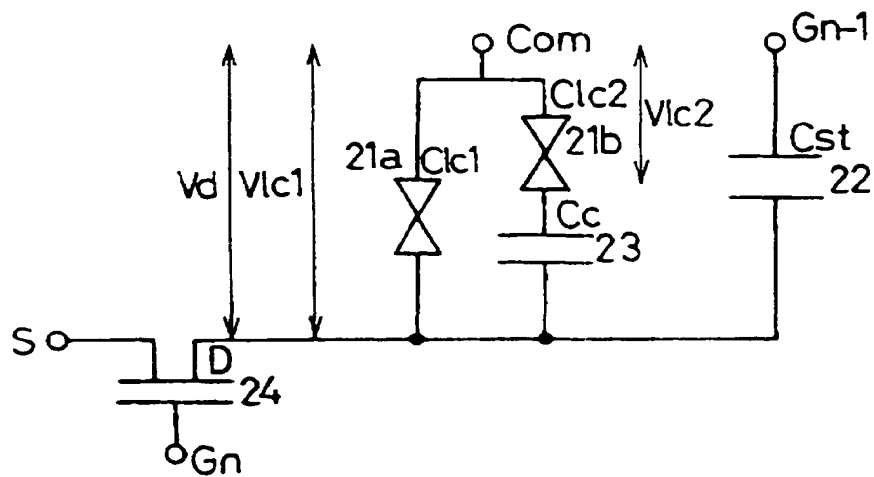


FIG. 1 (c)



European Patent
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EUROPEAN SEARCH REPORT

Application Number

EP 95 10 8260

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
D,X	US-A-5 245 450 (UKAI YASUHIRO ET AL) 14 September 1993 * column 5, line 17 - line 55 * * column 6, last paragraph - column 7, paragraph 1 *	1,2	G02F1/1343 G02F1/136
A	* column 8, line 26 - line 59 * * column 10, line 38 - line 45; figure 13 *	3,9	
A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 33, no. 1A, 1 June 1990, page 481/482 XP000117810 "THIN FILM TRANSISTOR FOR GRAY SCALE LCD" * the whole document *	1,2	
A	PROCEEDINGS OF THE INTERNATIONAL DISPLAY RESEARCH CONFERENCE, SAN DIEGO, OCT. 15 - 17, 1991, no. CONF. 11, 15 October 1991, INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, pages 255-257, XP000571463 SUNATA T ET AL: "A WIDE-VIEWING-ANGLE 10-INCH-DIAGONAL FULL-COLOR ACTIVE-MATRIX LCD USING A HALFTONE-GRAYSCALE METHOD" * page 255, left-hand column - page 256, right-hand column; figures 1A,1B,2 *	3	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			G02F
A	EP-A-0 357 463 (SHARP KK) 7 March 1990 * page 6, line 31 - line 35 * * page 8; figures 2,8,9 *	3,7,9	
D,A	US-A-4 840 460 (BERNOT ANTHONY J ET AL) 20 June 1989 * column 4, last paragraph - column 5, paragraph 1; figure 5 *	7,8	
A	EP-A-0 562 120 (HOSIDEN CORP) 29 September 1993 -----		
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 18 October 1996	Examiner Wongel, H
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone : particularly relevant if combined with another document of the same category A : technological background : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date : document cited in the application : document cited for other reasons : member of the same patent family, corresponding document</p>			

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CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

- ☐ All claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid,
namely claims:
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions,

namely:

- see sheet B -

- ☒ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid,
namely claims:
- ☐ None of the further search fees has been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims,
namely claims:



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LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims 1-2: A liquid crystal display comprising a thin film transistor subpixels and a control capacitor one electrode of which is formed at the same time as a source electrode and a drain electrode of the thin film transistor
2. Claims 3-6: A liquid crystal display comprising subpixels wherein the ratio of the display areas of the subpixels and the difference in driving voltages of the subpixels are controlled so as to provide a smooth voltage-light transmission curve
3. Claims 7-8: A liquid crystal display comprising subpixels having different driving voltages wherein the angle ϕ between the long axis of liquid crystal molecules in the middle of the liquid crystal layer and the boundary between subpixels is $0^\circ \leq \phi \leq 45^\circ$
4. Claims 9-11: A liquid crystal display comprising subpixels having different driving voltages wherein the gap between the subpixels is two times as thick as the liquid crystal layer or less

